

## Решење испита 16.01.2017.

1)

```
LOAD #0000h
STORE MemSem0
LOAD #0100h
STORE MemCntB
OUT FF03h
LOAD #0600h
STORE MemDstB
LOAD #0500h
OUT FF05h
LOAD #0080h
OUT FF20h
OUT FF10h
LOAD #0082h
OUT FF00h

Loop1: IN FF21h
AND #8000h
JZ Loop1
IN FF22h
STORE (MemDstB)
INC MemDstB
DEC MemCntB
JNZ Loop1
LOAD #0000h
OUT FF20h

Wait0: LOAD MemSem0
AND #0001h
JZ Wait0

...
LOAD #0100h
STORE MemCnt
LOAD #0500h
STORE MemA
LOAD #0600h
STORE MemB
LOAD #0700h
STORE MemC

Loop: LOAD (MemA)
SUB (MemB)
JL MinA
LOAD (MemB)
JMP StoreC

MinA: LOAD (MemA)
StoreC: STORE (MemC)
INC MemA
INC MemB
INC MemC
DEC MemCnt
JNZ Loop

LOAD #0000h
```

```
STORE MemSem2
LOAD #0100h
STORE MemCnt
LOAD #0700h
STORE MemC
LOAD #0086h
OUT FF30h
```

...

```
Wait2: LOAD MemSem2
      AND #0001h
      JZ Wait2
      HALT
```

Прекидна рутина за PER2:

```
PUSHA
LOAD (MemC)
OUT FF32h
INC MemC
DEC MemCnt
JNZ Back
LOAD #0001h
STORE MemSem2
LOAD #0000h
OUT FF30h
```

```
Back: POPA
      RTI
```

Прекидна рутина за DMA:

```
PUSHA
LOAD #0000h
OUT FF10h
OUT FF00h
LOAD #0001h
STORE MemSem0
POPA
RTI
```