

WHERE PASSION LEADS TO EXCELLENCE



ELSYS
EASTERN EUROPE

MEMBER OF  **ADVANS
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Dragi studeti/kolege,

Polako dolazi trenutak za početak razmišljanja o tome kuda želite da usmerite svoju karijeru. ELSYS pruža mogućnost da je usmerite u pravcu razvoja čipova već tokom ove zime i proleća!

Raspisujemo konkurs za nove članove našeg tima u vidu prakse i stalnog zaposlenja u Beogradu. Otvoreno je 10 pozicija u različitim domenima:

- Digital Design Verification Engineer
- Digital IC Design Engineer
- Software Developer in Test Engineer
- Analog Custom IC Layout Engineer
- Analog Mixed-Signal Design and Verification Engineer
- Silicon Validation/Characterization Engineer

Obuka može biti tema završne teze u dogovoru sa predmetnim profesorom. Rok za prijavu je **10. januar**, a vaše biografije i motivaciona pisma pošaljite na adresu jobs@elsys-eastern.com sa oznakom za koje pozicije konkurišete.

Za dodatne informacije, budite slobodni da me kontaktirate.

Pozdrav

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Ivica MILADINOVIC
Business Manager

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For our offices in **Belgrade, Serbia**, we are looking for:

Digital Design Verification Engineer 6 positions - Internship and permanent job junior positions

Following good practice from the past years, ELSYS Eastern Europe is announcing open positions for the winter/spring 2018 internships and entry level permanent positions.

Description:

You will be a part of a digital/mixed-signal design verification team, constituted of verification engineers with different level of experience, from verification experts to verification juniors. In order to achieve IC implementation milestones you will be involved in verification methodology establishment, verification plan creation, development of verification environment and close interaction with other IC implementation teams. Your work on these activities will be challenging and you will have the opportunity to work with industry experts on the state-of-the-art processes, tools and flows.

- Selected candidates will be engaged in development of a digital/mixed-signal verification environment including:
 - architecture definition
 - verification environment implementation in SystemVerilog
 - verification planning
 - full verification in accordance with UVM methodology (SystemVerilog)
- Selected candidates will receive full training and support from our experienced engineers
- Project can be subject of a bachelor/master thesis or seminar paper
- Internship positions details
 - Full time 40h a week during 3 months, starting February 2018 or differently defined with candidate with ability to attend university obligations during this period
 - Monthly compensation and accommodation provided if they are outside of Belgrade/Novi Sad

Qualification Requirements:

- Final year student or fresh graduate with B.Sc. or M.Sc. degree in electrical engineering
- Motivated, proactive, hard working
- Relevant courses/knowledge: basics of digital electronics, programming, FPGA basics, microcontroller architecture
- Skills: VHDL or Verilog, C programming language, object-oriented programming is a plus
- Good knowledge of English language is a plus

Employee Benefits:

- Integration Program in a Professional, Young & Dynamic Team
- Professional Development Opportunities
- Competitive Salaries & Benefits
- Compensation package includes also Additional Health Insurance, Sport & Social activities
- International Work Environment & Traveling Opportunities

All interested candidates are invited to send **CV and motivation letter in English**, indicating the field of interest, relevant college courses and projects, on the following email: jobs@elsys-eastern.com. Deadline for application is **10th January**. Interviews are planned for the **December and January** and the final list of selected candidates will be announced by **31st January**.

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For our office in **Belgrade**, Serbia, we are offering:

Digital IC Design Engineer 1 position - Internship and permanent job positions

Following good practice from the past years, ELSYS Eastern Europe is announcing open positions for the winter/spring 2018 internships and entry level permanent positions.

Internship description:

You will be a part of a digital design team, constituted of engineers with different level of experience, from experts to juniors. Your duties on the project will include, but will not be limited to:

- Digital design IP architecture development;
- Hardware description in VHDL/Verilog ;
- Pre-synthesis and post-synthesis tools flow setup/run;
- IP Verification using direct verification approach;
- Intern will receive full training and support from our experienced engineers.
- Internship duration is 3 months, starting in February 2018 or differently defined with candidate
- Full time internship 40h a week or differently defined with candidate
- Internship project can be subject of a bachelor/master thesis or seminar paper
- Interns will receive monthly compensation and company will provide accommodation if they are outside of Belgrade

Required Skills and Qualifications:

- Final year student or fresh graduate with B.Sc. or M.Sc. degree in electrical engineering
- Motivated, proactive, hard working
- Relevant courses/knowledge: basics of digital electronics, VLSI basics, Architectures of Micro Computing Systems;
- Skills: Familiar with VHDL or Verilog HDL language, Cadence tools knowledge **is a plus**
- Good knowledge of English language

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For our office in **Belgrade**, Serbia, we are offering:

Software Developer Engineer in Test Internship 1 position - Internship and permanent job positions

Internship description:

You will be part of the team working on complex SW test system in cutting edge technologies for the world class company. Test bench is comprised of hundreds of different microcontrollers arranged in in-house designed test racks and accessible from SW test bench which is supported by databases, different web technologies, continuous integration system, version control system as well as complex Python multi-process framework running on about 10 different operating systems (different versions of Windows, Linux and Mac OSX). You will work in the dynamic and modern environment with the highly skilled, enthusiastic people willing to share their knowledge with you.

- Selected candidates will be engaged in development of automated validation environment including:
 - Develop automated test framework and regression test suites in Python to optimize validation efficiency
 - Develop and execute tests on the test bench
 - Provide and analyze test results and identify failures/defects
 - Collaborate closely with SW / Systems engineers and Application
- February 2018 Intern will receive full training and support from our experienced engineers
- Internship duration is 3-6 months, starting in February 2018 or differently defined with candidate
- Full time internship 40h a week or differently defined with candidate
- Internship project can be subject of a bachelor/master thesis or seminar paper
- Interns will receive monthly compensation and company will provide accommodation if they are outside of Belgrade

Required Skills and Qualifications:

- Final year student or fresh graduate with B.Sc. or M.Sc. degree in electrical engineering
- Motivated, proactive, hard working
- Knowledge of Python programming language
- Knowledge of databases (SQL) and computer networks
- WEB development skills (HTML, CSS, PHP, JavaScript, etc.)
- Knowledge of microcontroller architecture
- Able to work in Windows, Linux and Mac environment
- Fluent in English
- Knowledge of C, C++, C#, Java or similar language is a plus
- Mobility is a plus

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For our offices in **Belgrade, Serbia**, we are looking for:

Analog Custom IC Layout Engineer **2 positions - Internship**

Following good practice from the past years, ELSYS Eastern Europe is announcing open positions for the winter/spring 2018 internships and entry level permanent positions.

Description:

You will be a part of analog layout team, constituted of engineers with different level of experience, from analog layout experts to juniors. Your work on these activities will be challenging and you will have the opportunity to work with industry experts on the state-of-the-art processes, tools and flows.

- Interns will be engaged in analog layout design including:
 - Full custom analog IC layout design methodologies
 - Using Cadence tools for layout design
 - Layout design of several common analog circuits
 - Using Cadence tools for layout design verification (DRC/LVS)
- Selected candidates will receive full training and support from our experienced engineers
- Project can be subject of a bachelor/master thesis or seminar paper
- Internship positions details
 - Full time 40h a week during 3 months, starting February 2018 or differently defined with candidate with ability to attend university obligations during this period
 - Monthly compensation and accommodation provided if they are outside of Belgrade/Novi Sad

Qualification Requirements:

- Final year student or fresh graduate with B.Sc. or M.Sc. degree in electrical engineering
- Motivated, proactive, hard working
- Relevant courses/knowledge: basics of analog electronics, VLSI basics
- Skills: Familiar with basic analog IC design/layout, CMOS technology and IC manufacturing concepts, Cadence tools knowledge is a plus
- Good knowledge of English language is a plus

Employee Benefits:

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For our offices in **Belgrade, Serbia**, we are looking for:

Analog Mixed-Signal Design and Verification Engineer 2 positions - Internship and permanent job positions

Following good practice from the past years, ELSYS Eastern Europe is announcing open positions for the winter/spring 2018 internships and entry level permanent positions.

Description:

You will be a part of an analog design and modeling team, constituted of engineers with a different level of experience, from experts to juniors. Involvement in analog IP models development, model calibration (model vs schematic) and close interaction with other IC implementation teams in order to achieve IC implementation milestones. Your work on these activities will be challenging and you will have the opportunity to work with industry experts on the state-of-the-art processes, tools, and flows. Selected candidates will receive full training and support from our experienced engineers. The project can be a subject of a bachelor/master thesis or seminar paper.

- Selected candidates will be engaged in:
 - Development of Verilog A/AMS models of AMS IP
 - Analog and mixed-signal circuits analysis
 - Development of AMS design verification environment
 - Using Cadence toolchain for AMS verification
- Selected candidates will receive full training and support from our experienced engineers
- Project can be subject of a bachelor/master thesis or seminar paper
- Internship positions details
 - Full time 40h a week during 3 months, starting February 2018 or differently defined with candidate with ability to attend university obligations during this period
 - Monthly compensation and accommodation provided if they are outside of Belgrade

Qualification Requirements:

- Final year student or fresh graduate with B.Sc. or M.Sc. degree in electrical engineering
- Motivated, proactive, hard working
- Relevant courses/knowledge: basics of analog electronics, VLSI basics
- Skills: Familiar with basic analog circuitry, CMOS, and bipolar technology, VHDL or Verilog HDL language, Cadence tools knowledge is a plus
- Good knowledge of English language

Employee Benefits:

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For our office in Belgrade, Serbia, we are offering:

Silicon Validation/Characterization Engineer **1 positions - Internship and permanent job position**

Internship description:

You will be part of the team working on **ADC integrated circuit** in cutting edge technologies developed at School of Electrical Engineering. In order to achieve IC implementation milestones you will be involved in validation methodology establishment, validation plan creation, development of validation environment and close interaction with other IC implementation team. Your work on these activities will be challenging and you will have the opportunity to work with industry experts on the state-of-the-art processes, tools and flows.

- Selected candidates will be engaged in development of automated validation environment including:
 - Testboard development - PCB Design
 - Develop and execute tests on the lab bench according to validation plan during post-silicon phases of the product to ensure functionality and performance as per the datasheet
 - Develop automated test framework and regression test suites in LabVIEW/TestStand to optimize validation efficiency
 - Collaborate closely with IC Design engineers, Systems engineers and Application engineers to understand requirements and use cases.
 - Debug analog/digital silicon issues, board issues and Validation software/hardware tools issues
 - Analyze test results and identify failures/defects
- Intern will receive full training and support from our experienced engineers.
- Internship duration is 3-6 months, starting in February 2018 or differently defined with candidate
- Full time internship 40h a week or differently defined with candidate
- Internship project can be subject of a bachelor/master thesis or seminar paper
- Interns will receive monthly compensation and company will provide accommodation if they are outside of Belgrade

Required Skills and Qualifications:

- Final year student or fresh graduate with B.Sc. or M.Sc. degree in electrical engineering
- Motivated, proactive, hard working
- Relevant courses/knowledge: basics of digital electronics, programming, FPGA basics, microcontroller architecture
- VHDL or Verilog, C programming language, object-oriented programming is a plus
- Good knowledge of English language **is a plus**
- Experience with scripting or LabVIEW **is a plus**
- Experience in designing mixed signal boards **is a plus**
- Experience in lab equipment **is a plus**

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