

# WHERE PASSION LEADS TO EXCELLENCE



Based in Belgrade, ELSYS Eastern Europe is a subsidiary of ADVANS Group which is a European group of experts in electronic systems and software. Our 1000 engineers serve a range of industries: in multimedia/wireless, service, medical, network/telecom, energy, aerospace, defense, automotive and IT.

For our offices in **Belgrade and Novi Sad**, Serbia, we are looking for:

## Digital Design Verification Engineer 12 positions - Internship and permanent job junior positions

ELSYS Eastern Europe is announcing open positions for the winter/spring 2019 internships and entry level permanent positions.

### Description:

You will be a part of a digital/mixed-signal design verification team, constituted of verification engineers with different level of experience, from verification experts to verification juniors. In order to achieve IC implementation milestones you will be involved in verification methodology establishment, verification plan creation, development of verification environment and close interaction with other IC implementation teams. Your work on these activities will be challenging and you will have the opportunity to work with industry experts on the state-of-the-art processes, tools and flows.

- Selected candidates will be engaged in development of a digital/mixed-signal verification environment including:
  - architecture definition
  - verification environment implementation in SystemVerilog
  - verification planning
  - full verification in accordance with UVM methodology (SystemVerilog)
- Intern will receive full training and support from our experienced engineers.
- Internship duration is 3 months, with two starting dates:
  - **Round 1 - 3<sup>rd</sup> December**
  - **Round 2 - 18<sup>th</sup> February**
  - or differently defined with candidate
- Full time internship 40h a week or differently defined with candidate
- Internship project can be subject of a bachelor/master thesis or seminar paper
- Interns will receive monthly compensation
- Company will provide accommodation if interns are outside of Belgrade

### Qualification Requirements:

- Final year student or fresh graduate with B.Sc. or M.Sc. degree in electrical engineering
- Motivated, proactive, hard working
- Relevant courses/knowledge: basics of digital electronics, programming, FPGA basics, microcontroller architecture
- Skills: VHDL or Verilog, C programming language, object-oriented programming is a plus
- Good knowledge of English language is a plus

### Employee Benefits:

- Integration Program in a Professional, Young & Dynamic Team
- Professional Development Opportunities
- Competitive Salaries & Benefits
- Compensation package includes also Additional Health Insurance, Sport & Social activities
- International Work Environment & Traveling Opportunities

All interested candidates are invited to apply on following link [www.elsys-eastern.com/career/internships](http://www.elsys-eastern.com/career/internships) with **CV and motivation letter in English**, indicating the field of interest, relevant college courses and projects. Deadline for application: **Round 1 - 17<sup>th</sup> November & Round 2 - 22<sup>nd</sup> December**. Interviews are planned from end of October; the final list of selected candidates will be announced by **Round 1 - 22<sup>nd</sup> November & Round 2 - 15<sup>th</sup> January**.

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MEMBER OF  ADVANS GROUP

ELSYS Eastern Europe, having its business in Belgrade and Novi Sad, Serbia, is a subsidiary of ADVANS Group. ADVANS Group is an international group of experts in software, electronic systems and mechanics. Our 1000 engineers serve a range of industries in: multimedia/wireless, medical, network/telecom, energy, aerospace, defense, automotive and IT.

For our office in **Belgrade and Novi Sad**, Serbia, we are offering:

## Digital IC Design Engineer 2 position - Internship and permanent job positions

ELSYS Eastern Europe is announcing open positions for the winter/spring 2019 internships and entry level permanent positions.

### Description:

You will be a part of a digital design team, constituted of engineers with different level of experience, from experts to juniors. Your duties on the project will include, but will not be limited to:

- Digital design IP architecture development
- Hardware description in VHDL/Verilog
- Pre-synthesis and post-synthesis tools flow setup/run
- IP Verification using direct verification approach
- Intern will receive full training and support from our experienced engineers
- Internship duration is 3 months, with two starting dates:
  - **Round 1 - 3<sup>rd</sup> December**
  - **Round 2 - 18<sup>th</sup> February**
  - or differently defined with candidate
- Full time internship 40h a week or differently defined with candidate
- Internship project can be subject of a bachelor/master thesis or seminar paper
- Interns will receive monthly compensation
- Company will provide accommodation if interns are outside of Belgrade

### Required Skills and Qualifications:

- Final year student or fresh graduate with B.Sc. or M.Sc. degree in electrical engineering
- Motivated, proactive, hard working
- Relevant courses/knowledge: basics of digital electronics, VLSI basics, Architectures of Micro Computing Systems;
- Skills: Familiar with VHDL or Verilog HDL language, Cadence tools knowledge **is a plus**
- Good knowledge of English language

### Employee Benefits:

- Integration Program in a Professional, Young & Dynamic Team with Professional Development Opportunities
- Competitive Salaries & Benefits
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For our office in **Belgrade**, Serbia, we are offering:

## Embedded Software Engineer 4 positions - Internship and permanent job positions

ELSYS Eastern Europe is announcing open positions for the winter/spring 2019 internships and entry level permanent positions.

### Internship description:

You will be a part of embedded system team, constituted of engineers with different level of experience, from experts to juniors. Your duties on the project will include, but will not be limited to:

- Development of software for embedded systems (drivers, applications)
- Operating system configuration & porting
- Implementation/debugging of protocols with real-time constrains
- Providing support for customers all over the world
- Intern will receive full training and support from our experienced engineers.
- Internship duration is 3 months, with two starting dates:
  - **Round 1 - 3<sup>rd</sup> December**
  - **Round 2 - 18<sup>th</sup> February**
  - or differently defined with candidate
- Full time internship 40h a week or differently defined with candidate
- Internship project can be subject of a bachelor/master thesis or seminar paper
- Interns will receive monthly compensation
- Company will provide accommodation if interns are outside of Belgrade

### Required Skills and Qualifications:

- Final year student or fresh graduate with B.Sc. or M.Sc. degree in electrical engineering
- Motivated, proactive, hard working
- Relevant courses/knowledge: micro-computing systems, networking, real-time programming
- Good knowledge of C language
- Basic knowledge of Unix/Linux operating systems
- Experience in embedded systems, bare metal or with an OS, is a plus
- Experience in version control tools is a plus
- Good knowledge of English language

### Employee Benefits:

- Integration Program in a Professional, Young & Dynamic Team with Professional Development Opportunities
- Competitive Salaries & Benefits
- Compensation package includes also Additional Health Insurance, Sport & Social activities
- International Work Environment & Traveling Opportunities

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